

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((die chip dice) same ((conductive near1 (pattern layer trace)) (wiring adj layer)) same (second near1 (polymer\$2 polyimide)) and (horizontal over) same (opening via vias) same ((passive adj device) capacitor resistor inductor)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/15 08:54
L2	1	((die chip dice) and ((conductive near1 (pattern layer trace)) (wiring adj layer)) and (second near1 (polymer\$2 polyimide)) and (horizontal over) and (opening via vias) and ((passive adj device) capacitor resistor inductor)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/15 08:55
L3	7115	257/723-724,774.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/15 08:55
L4	1001	257/e23.174,e23.177.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/15 08:56
L5	7977	3 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/15 08:56
L6	2009	5 and (opening via vias) and ((passive adj device) capacitor resistor inductor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/15 08:57
L7	1555	6 and (toc (tape adj chip))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/15 08:58

## EAST Search History

L9	45	5 and (toc (tape adj1 chip))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/15 08:58
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